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Art Unit 2822
Attn: Mail Stop RCE

Re: U.S. Utility Patent Application
Appl. No. 10/780,877; Filing Date: February 19, 2004
For: **Overlay Correction By Reducing Wafer Slipping After Alignment**
Inventor: Peter KOCHERSPERGER
Our Ref: 1857.2140000

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Online Credit Card Payment Authorization in the amount of \$810.00 to cover: \$810.00 RCE Filing Fee;
2. Request for Continued Examination (RCE) Transmittal; and
3. Submission Under 37 C.F.R. § 1.114 and Preliminary Amendment and Reply Under 37 C.F.R. § 1.115.

The above-listed documents are filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

Fee payment is provided through online credit card payment. The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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JDE/RGH/lam
Enclosures

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